Sheet 1 of 1

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

OIPE JC25 SEP 28 2004 PATENT & TRADEMARK OFFICE

ATTY. DOCKET NO.	SERIAL NO.
550-522	10/779,809
APPLICANT	
MUDGE et al	
FILING DATE	**TC/A.U.**
February 18, 2004	~~2818~~ 2824

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SD		5,203,003	4/1993	Donner			
SD		6,476,643	11/2002	Hugues et al			
SD		6,188,610	2/2001	Kakizoe et al			
SD		2001/0016927	8/2001	Poisner			
SD		2002/0038418	3/2002	Shimamura			

FOREIGN PATENT DOCUMENTS

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
SD		WO 00/54410	9/2000	WIPO				
SD		0 366 331	5/1990	EP				
SD		0 374 420	6/1990	EP				
SD		0 653 708	5/1995	EP				
SD		60-20398	2/1985	JP			ABSTRACT	
SD		62-24498	2/1987	JP			ABSTRACT	
SD		809350	2/1981	SU			ABSTRACT	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

*Examiner	SON DINH	Date Considered	4/4/05

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INFORMATION DISCLOSURE CITATION

U.S. PATENT & TRADEMARK OFFICE JUL 28 2004 JC80

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SD		5,615,263	3/1997	Takahashi			
SD		5,627,412	5/1997	Beard			

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		WO 01/46800	6/2001	WIPO				

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SD		"ARM710 Data Sheet" 12/1994, Advanced RISC Machines Ltd. (ARM)
SD		F. Worm et al, "An Adaptive Low-Power Transmission Scheme for On-Chip Networks" *ISSS'02*, 10/2002, pages 92-100

*Examiner	SON DINH	Date Considered	4/4/05

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